## What Is Claimed Is:

1. A liquid crystal display panel, comprising:

a first substrate;

an image display part formed on the first substrate and having a plurality of pixels arranged thereon;

a plurality of gate and source drivers for supplying signals to the pixels;
a controller for supplying control signals to the gate and source drivers;
at least one conductive line at a corner portion of the first substrate, the
conductive line connecting the controller and the gate drivers; and

a plurality of transparent electrode segments overlapping the conductive line with at least one intermediate film interposed therebetween.

- 2. The panel according to claim 1, wherein a direction of the conductive line is parallel with a direction of the overlapping transparent electrode segments.
- 3. The panel according to claim 1, wherein the conductive line transmits DC signals including a gate high voltage (Vgh), a gate low voltage (Vgl), a common voltage (Vcom), a ground voltage (GND), and a power supply voltage (Vcc), and

1-WA/2054939.1 26

transmit AC signals including a gate start pulse (GSP), a gate shift clock (GSC), and a gate enable signal (GOE).

- 4. The panel according to claim 1, wherein a gate insulation film is applied as the intermediate film.
- 5. The panel according to claim 1, wherein a triple-film formed by stacking a gate insulation film, a semiconductor layer, and a passivation film is applied as the intermediate film.
- 6. The panel according to claim 5, wherein the passivation film includes an organic material having at least one of benzocyclobutene (BCB), a spin-on-glass (SOG), and photoacryl.
- 7. The panel according to claim 1, wherein a pixel electrode is applied as the transparent electrode.

8. The liquid crystal display panel according to claim 1, further comprising a seal pattern attaching the first substrate and a second substrate together within a seal pattern region such that a portion of the conductive line is within the seal pattern region.